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42. (Amended) A fan system, comprising  
a fan including an electric motor (10)  
whose speed is dependent on a commutation signal supplied thereto;  
a writable memory (14) integral with the fan and containing  
fan control information;  
a microcontroller integral with, and which outputs a  
commutation signal to, the electric motor of said fan, in  
accordance with control information stored in the writeable memory  
(14);  
a host computer (11); and  
an interface (13) between the host computer (11) and the  
microcontroller (12);  
the host computer providing different control information to  
the memory (14) via the interface (13) when said microcontroller  
(12) outputs different commutation signals in accordance with the  
control information currently stored in the memory (14) and  
supplied thereto by the host computer (11).

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